



Phase Control Thyristor

DS6063-1 April 2011 (LN28303)

FEATURES

- Double Side Cooling
- High Surge Capability

APPLICATIONS

- High Power Drives
- High Voltage Power Supplies
- Static Switches

VOLTAGE RATINGS

Part and Ordering Number	Repetitive Peak Voltages V _{DSM} and V _{RSM} V	Conditions
DCR4540H52 DCR4540H50 DCR4540H48 DCR4540H46	5200 5000 4800 4600	$\begin{split} T_{vj} = -40^{\circ}\text{C to } 110^{\circ}\text{C}, \\ I_{DRM} = I_{RRM} = 700\text{mA}, \\ V_{DRM}, V_{RRM} t_p = 10\text{ms}, \\ V_{DRM} \& V_{RRM} = \\ V_{DSM} \& V_{RSM} - 600V \\ respectively \end{split}$

Lower voltage grades available.

ORDERING INFORMATION

When ordering, select the required part number shown in the Voltage Ratings selection table.

For example:

DCR4540H52

Note: Please use the complete part number when ordering and quote this number in any future correspondence relating to your order.

KEY PARAMETERS

V_{DRM}	5200 V
$I_{T(AV)}$	4540 A
I _{TSM}	80000 A
dV/dt*	2000 V/µs
dI/dt	200 A/μs

* Higher dV/dt selections available

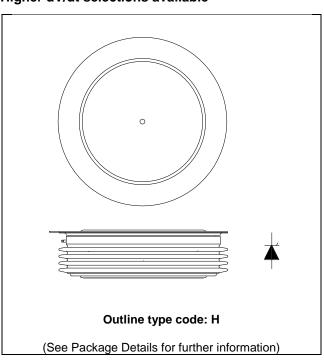


Fig. 1 Package outline



CURRENT RATINGS

T_{case} = 60°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units	
Double Si	Double Side Cooled				
I _{T(AV)}	Mean on-state current	Half wave resistive load	4540	А	
I _{T(RMS)}	RMS value	-	7130	А	
I _T	Continuous (direct) on-state current	-	6420	А	

SURGE RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
I _{TSM}	Surge (non-repetitive) on-state current	10ms half sine, $T_{case} = 110^{\circ}C$	80.0	kA
l ² t	I ² t for fusing	$V_R = 0$	32.00	MA ² s

THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Test Conditions	6	Min.	Max.	Units
R _{th(j-c)}	Thermal resistance – junction to case	Double side cooled	DC	-	0.004	°C/W
R _{th(c-h)}	Thermal resistance – case to heatsink	Double side cooled	DC	-	0.0008	°C/W
T_{vj}	Virtual junction temperature	Blocking V _{DRM} / _{VRRM}		-40	110	°C
T _{stg}	Storage temperature range			-40	140	°C
F _m	Clamping force			110	130	kN



DYNAMIC CHARACTERISTICS

Symbol	Parameter	Test Conditions		Min.	Max.	Units
I _{RRM} /I _{DRM}	Peak reverse and off-state current	At V _{RRM} /V _{DRM} , T _{case} = 110°C		-	700	mA
dV/dt	Max. linear rate of rise of off-state voltage	To 67% V _{DRM} , T _j = 110°C, gate open		2000	-	V/µs
dl/dt	Rate of rise of on-state current	From 67% V _{DRM} to 4000A	Repetitive 50Hz	-	200	A/µs
		Gate source 30V, 10Ω,	Non-repetitive	-	1000	A/µs
		$t_r < 0.5 \mu s, T_j = 110 ^{\circ} C$				
V_{T}	On-state voltage	I _T = 3000A, T _{case} = 110°C			1.46	V
$V_{T(TO)}$	Threshold voltage	T _{case} = 110°C		-	1.08	V
r _T	On-state slope resistance	T _{case} = 110°C		-	0.127	mΩ
t _{gd}	Delay time	$V_D = 67\% \ V_{DRM}$, gate source 30V, 10Ω		-	3.0	μs
		$t_r = 0.5 \mu s, T_j = 25^{\circ}C$				
tq	Turn-off time	$T_j = 110^{\circ}\text{C}$, $V_R = 100\text{V}$, $dI/dt = 1.5\text{A}/\mu\text{s}$,		-	800	μs
		$dV_{DR}/dt = 20V/\mu s$ linear to 67% V_{DRM}				
Qs	Stored charge	$I_T = 2000A$, $tp = 1000us$, $T_j = 110$ °C, $dI/dt = 1.5A/\mu s$,		- -	5500	μC
lι	Latching current	$T_j = 25$ °C,		-	1	А
I _H	Holding current	$T_j = 25$ °C,		-	200	mA

GATE TRIGGER CHARACTERISTICS AND RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
V_{GT}	Gate trigger voltage	V _{DRM} = 5V, T _{case} = 25°C	2.6	V
V_{GD}	Gate non-trigger voltage	At 40% V _{DRM} , T _{case} = 110°C	TBD	V
I _{GT}	Gate trigger current	V _{DRM} = 5V, T _{case} = 25°C	400	mA
I _{GD}	Gate non-trigger current	At 40% V _{DRM} , T _{case} = 110°C	TBD	mA



CURVES

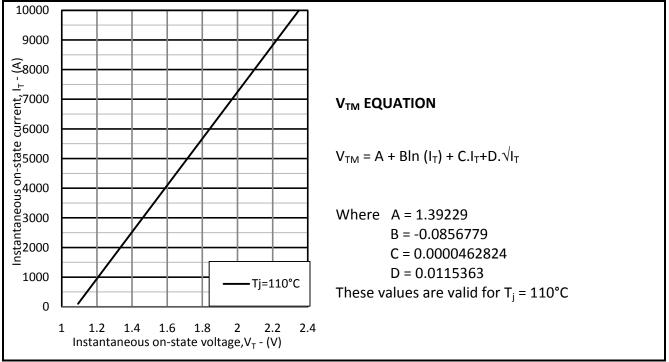


Fig.2 Maximum &minimum on-state characteristics

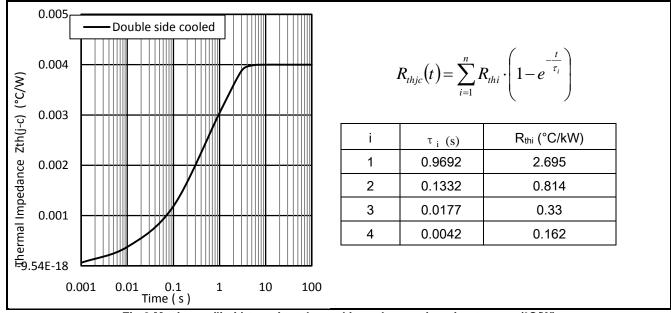
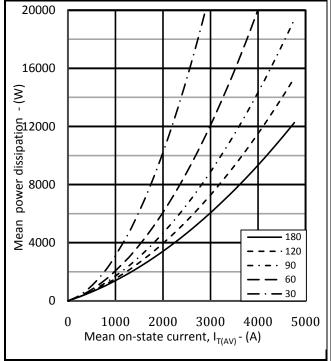


Fig.3 Maximum (limit) transient thermal impedance – junction to case (°C/W)





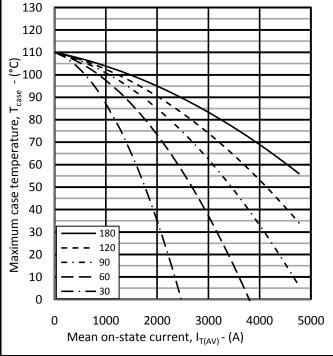


Fig.4 On-state power dissipation – sine wave

Fig.5 Maximum permissible case temperature, double side cooled – sine wave

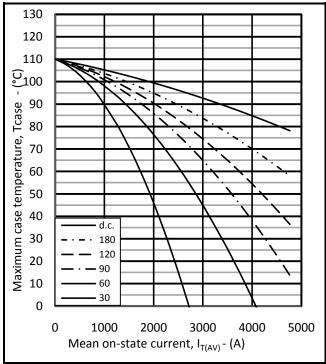


Fig.6 Maximum permissible case temperature, double side cooled – rectangular wave

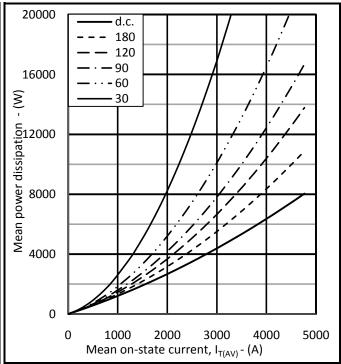
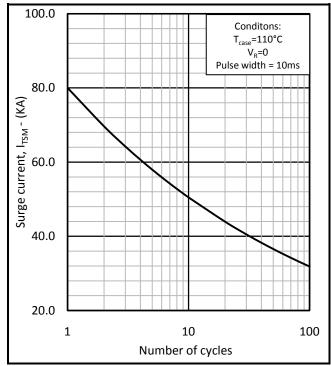


Fig.7 On-state power dissipation - rectangular wave





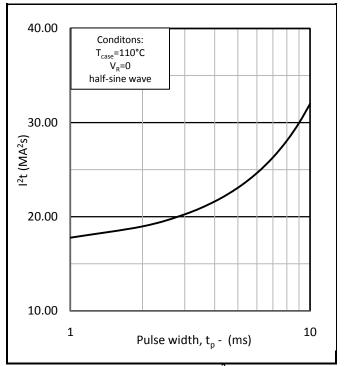


Fig.8 Multi-cycle surge current

Fig.9 Single-cycle I2t

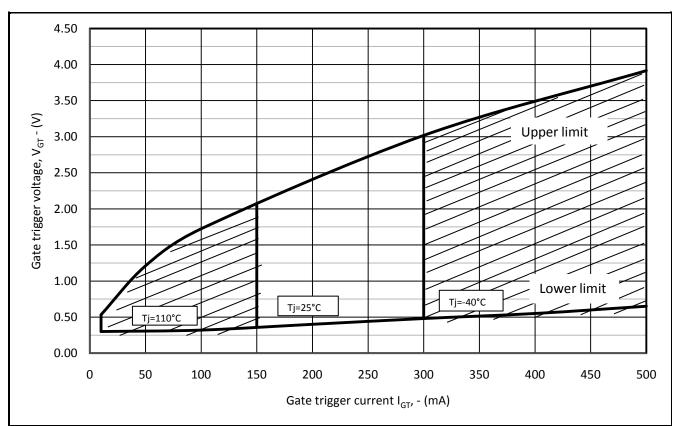


Fig.10 Gate characteristics



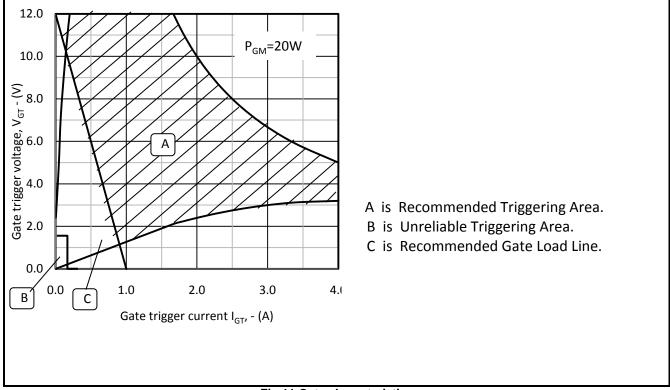


Fig.11 Gate characteristics



PACKAGE DETAILS

For further package information, please contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.

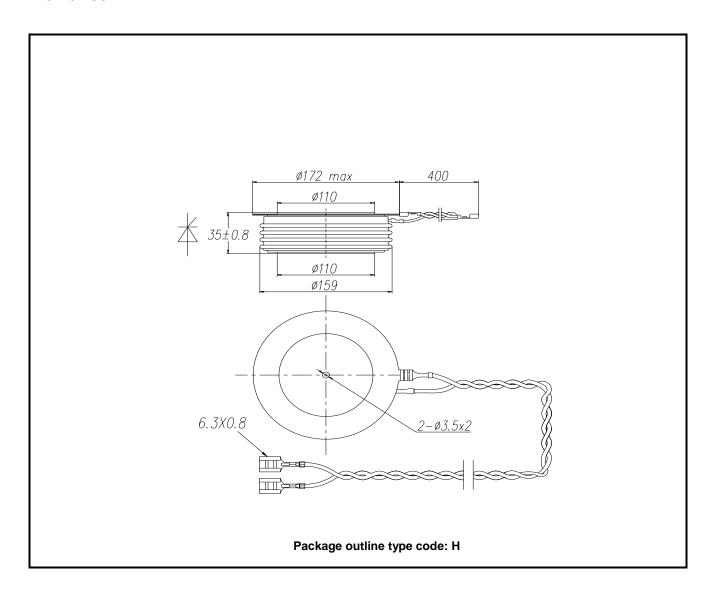


Fig.12 Package outline



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Preliminary Information:

No Annotation:

We annotate datasheets in the top right hand corner of the front page, to indicate product status if it is not yet fully approved for production. The annotations are as follows:-

Target Information: This is the most tentative form of information and represents a very preliminary specification.

No actual design work on the product has been started.

The product design is complete and final characterisation for volume production is in

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The product has been approved for production and unless otherwise notified by Dynex any product ordered will be supplied to the **current version of the data sheet prevailing at the**

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